**Brayden Currier**

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**Summary:**

I am a motivated and hard-working senior in Computer Engineering with a strong foundation in C++, C#, C, Assembly, Verilog, and a general understanding of PCB design, seeking an internship to apply technical skills and contribute to impactful projects where I can hone my skills learned in school.

**Education:**

**Cal Poly Pomona**

Bachelor of Science in Computer Engineering

Expected date of graduation, December of 2025

**Skills:**

* Strong understanding of C++, C#
* Some experience with Python
* Programing microcontrollers using C and assembly language
* PCB design in Altium
* Programing FPGAs in Vivado using Verilog
* Digital logic design
* Fundamental knowledge of UART and I2C protocols
* Use of oscilloscopes and logic analyzers
* Team collaboration
* Problem-Solving

**Projects:**

* Collaborated with a team of engineering students to propose a satellite project, conducting research and presenting technical design reviews to JPL and NASA employees for funding opportunities. Although we ultimately weren’t able to get funding to complete our project, I got valuable experience in collaborating with a team and creating design review presentations for the proposed project.
* Developed a C++ console application to solve five-letter Wordle puzzles by implementing search algorithms and deduction strategies. The application narrows down possible solutions by analyzing character frequency, position, and feedback from prior guesses. (The source code is on my website under projects)